

**Listing of Claims:**

Please cancel claims 1-18 without prejudice or disclaimer.

19. (New) An array substrate for in-plane switching mode liquid crystal display device, comprising:

a gate line and a common line in a horizontal direction on a substrate;  
a data line crossing the gate line and the common line;  
a pixel electrode and a common electrode on the substrate; and  
a thin film transistor at a cross point of the gate line and the data line, the thin film transistor having a gate electrode, an active layer and source and drain electrodes, an electric field direction between the gate and common electrodes being parallel with a rubbing direction.

20. (New) The array substrate according to claim 19, wherein the common electrode and the pixel electrode are formed in a zigzag pattern.

21. (New) The array substrate according to claim 19, wherein the source and drain electrodes are formed over a portion of the gate line.

22. (New) The array substrate according to claim 19, wherein the source electrode has an U-shape and the drain electrode is formed in an interior of the U-shape.

23. (New) The array substrate according to claim 19, wherein the drain electrode has an I-shape.

24. (New) The array substrate according to claim 19, wherein the rubbing direction of an alignment layer is substantially perpendicular to the gate electrode.

25. (New) The array substrate according to claim 19, wherein the pixel electrode has an extension portion, a plurality of vertical portions, and a horizontal portion.

26. (New) The array substrate according to claim 25, wherein the extension portion is extended from the drain electrode.

27. (New) The array substrate according to claim 26, wherein the vertical portions are

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vertically extended from the extended portion and are spaced apart from each other.

28. (New) The array substrate according to claim 25, wherein the horizontal portion is disposed over the common line and connects the plurality of vertical portions into one portion.

29. (New) The array substrate according to claim 19, wherein the common electrode has a plurality of vertical portions and a horizontal portion.

30. (New) The array substrate according to claim 29, wherein the vertical portions are vertically extended from the common line and arranged in an alternative pattern with the vertical portions of the pixel electrode.

31. (New) The array substrate according to claim 29, wherein the horizontal portion connects the vertical portions into one portion.

32. (New) The array substrate according to claim 29, wherein a horizontal portion of the common electrode has a side that is parallel with a slope of the gate electrode.

33. (New) The array substrate according to claim 19, wherein the gate electrode has a slope that satisfies a numeral expression  $|\theta_R - \theta_g| = 89^\circ \sim 91^\circ$  (degrees), wherein  $\theta_R$  is an angle of the rubbing direction  $\theta_g$  is an angle of the slope of the gate electrode.

34. (New) The array substrate according to claim 33, wherein the slope of the gate electrode is perpendicular to the rubbing direction.